

Education is a way to success in life

Bangladesh University of Business & Technology

Department of Computer Science and Engineering (Eve)



Lab Report

Course Title : Digital Logic Design Lab

Course code : CSE- 206

Exp. No.-	Experiment Name :
01	Implement Circuit with Basic Gates.
02	Simplification using Boolean Algebra.
03	Simplification using Karnaugh Map
04	Design a 1 bits Parallel Adder using 74283 IC.
05	Design a 4 bits Parallel Adder using 74283 IC.
06	Design a 4 bits Parallel Adder/Subtractor using 74283 IC.

Submitted To :

Sohel Rana

Lecturer

Bangladesh University of
Business & Technology (BUBT)

Submitted By :

Miraz Hossain

ID-16173203065

Progr. -B.Sc Engg in CSE (eve)
Intake - 27, Section - 2

Total Obtained Marks=

Date: 16-09-2018

Signature